

ABSTRACT OF THE DISCLOSURE

In a PDP driver, first and second signal lines for a voltage of V_s and third and fourth signal lines for a voltage of $0V$ are formed. While maintaining Y and X electrodes of the panel capacitor at V_s and $0V$, a first current path is formed from the first signal line to the fourth signal line through an inductor to supply a first-directional current to the inductor. A second current path is formed from the Y electrode to the X electrode through the inductor to change the Y and X electrode voltages using the resonance. When the electrode voltages become $0V$ and V_s , a third current path is formed from the third signal line to the second signal line through the inductor to reduce the first-directional current.